

Peak pulse voltage
($T_j=25$

ORDERING INFORMATION

J	ST	16	E	-1200	TW	-/
JieJie Microelectronics Co., Ltd.	Triacs	IT(RMS):16A	E:TO-263	1200:VDRM /VRRM 11200 V	TW:IGT1-3 05mA	Blank:Tube -TR:Tape &Reel

FIG.1: Maximum power dissipation versus RMS on-state current

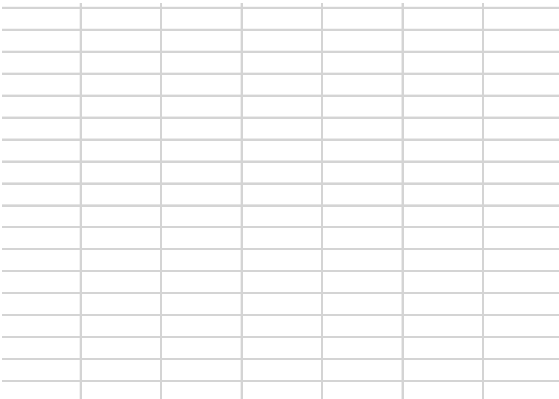


FIG.2: RMS on-state current versus case temperature

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

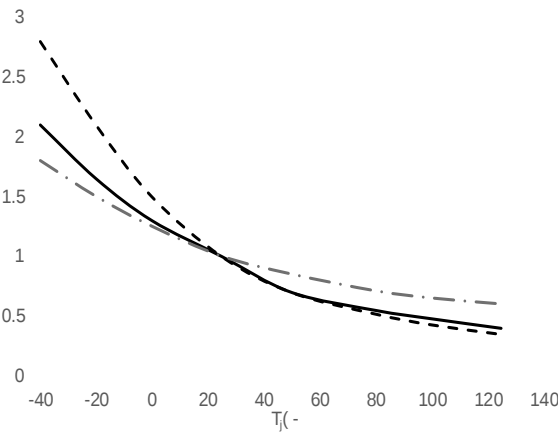


FIG.8: Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



ORDERING INFORMATION

Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		H- I- J			
JST16E-1200TW	1200	5	TO-263	50	Tube
JST16E-1200TW-TR				800	Tape & Reel

Document Revision History

Date	Revision	Changes
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DELIVERY MODES Q q 0.0250773 0 0 -0.0257975 T S cm 57487862 916521 461.1S0



