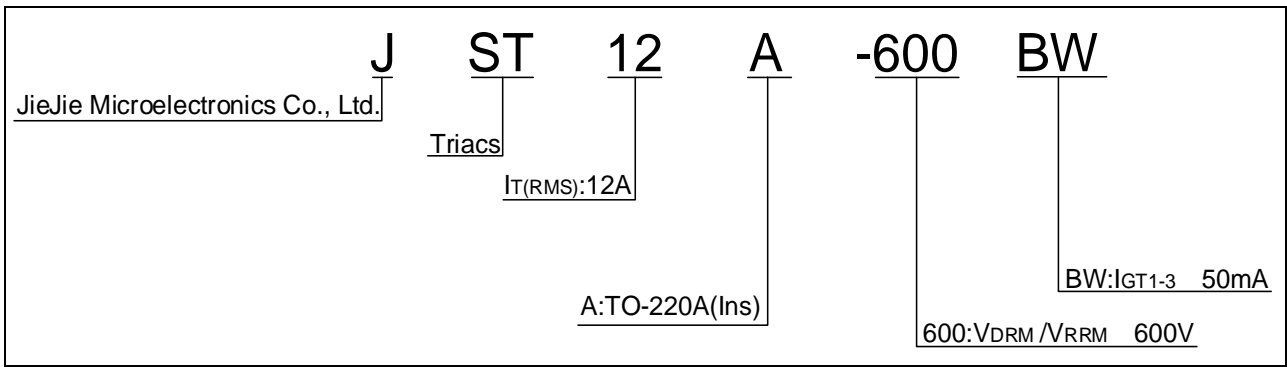


## DESCRIPTION:

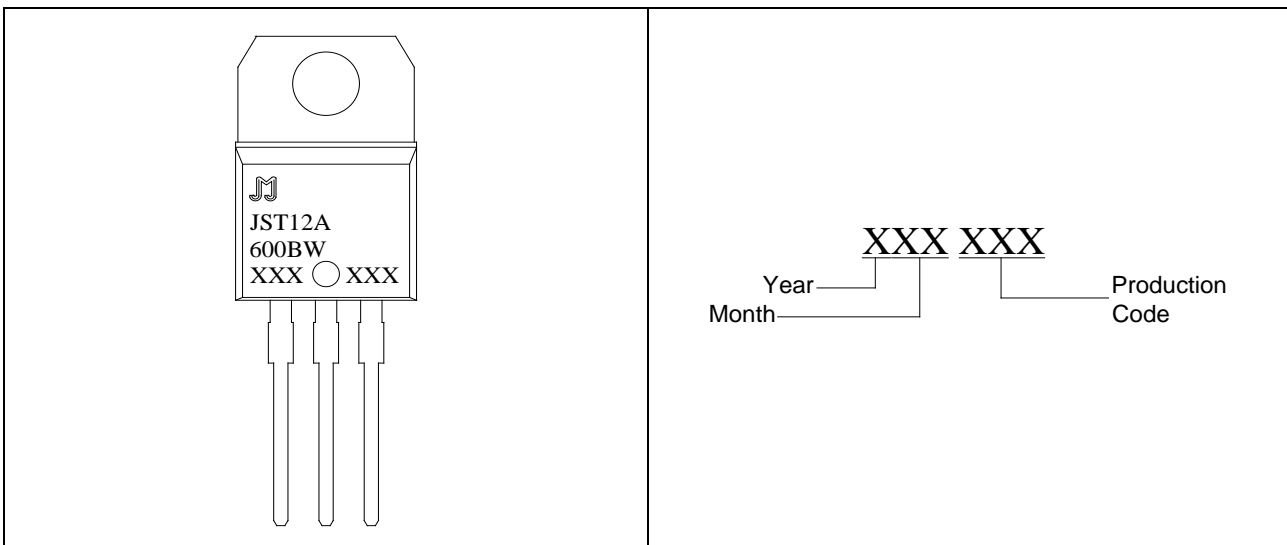
The JST12A, -600, BW triac is suitable for 0 (f)-8 -2.197 Td ac

$I_H$   $I_T=500\text{mA}$  . 70

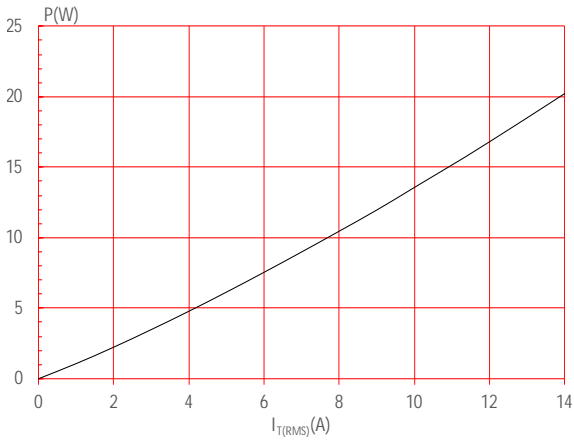
ORDERING INFORMATION



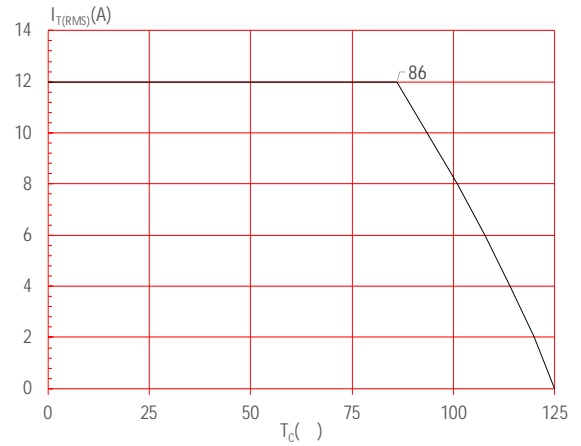
MARKING



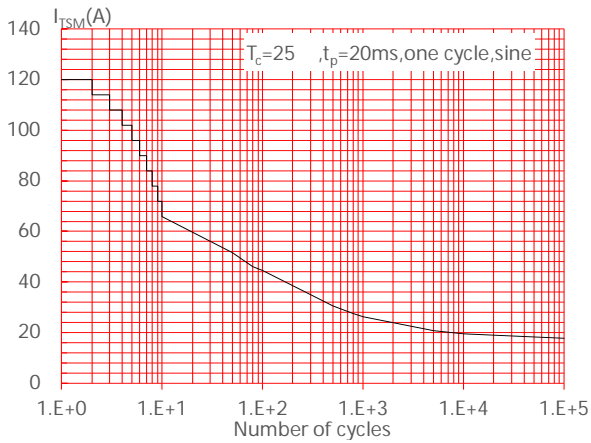
**FIG.1:** Maximum power dissipation versus RMS on-state current



**FIG.2:** RMS on-state current versus case temperature

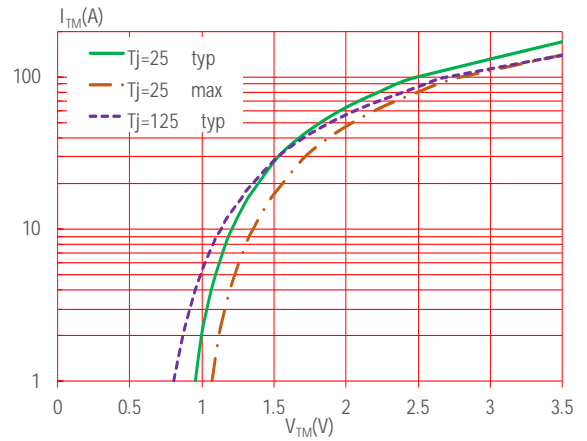


**FIG.3:** Surge peak on-state current versus number of cycles



**FIG.5:** Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 20ms$ , and corresponding value of  $I^2t$  ( $di/dt < 100A/\mu s$ )

**FIG.4:** On-state characteristics



**FIG.6:** Relative variations of gate trigger current, holding current and latching current versus junction temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards

**ORDERING INFORMATION**





