



**JST02V-800SW**

**JieJie Microelectronics Co., Ltd.**

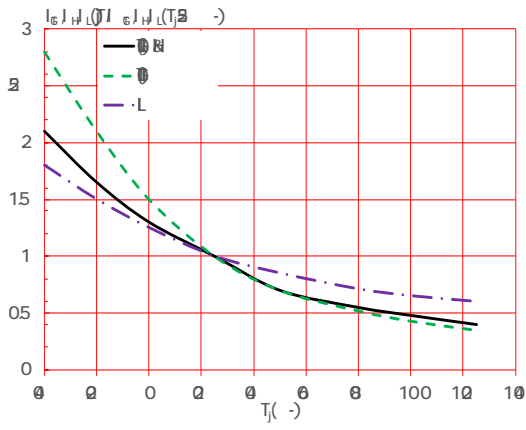
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FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature





**ORDERING INFORMATION**

Order code	Voltage $V_{DRM}/V_{RRM}$ (V)	
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**PACKAGE MECHANICAL DATA**

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