

JIEJIE MICROELECTRONICS CO., LTD.

MCR100 9 1 Tf -09 ()[(0.493 3.76 (0) 1)Tj -0..002 2.40.007 Tw 2As4 738.6iN4.0

Peak gate power	P_{GM}	2	W
Peak pulse voltage ($T_j=25^\circ\text{C}$; non-repetitive,off-state;FIG.8)	V_{pp}	1	kV

NOTE 1: Operating junction temperature T_j is up to 125

MARKING

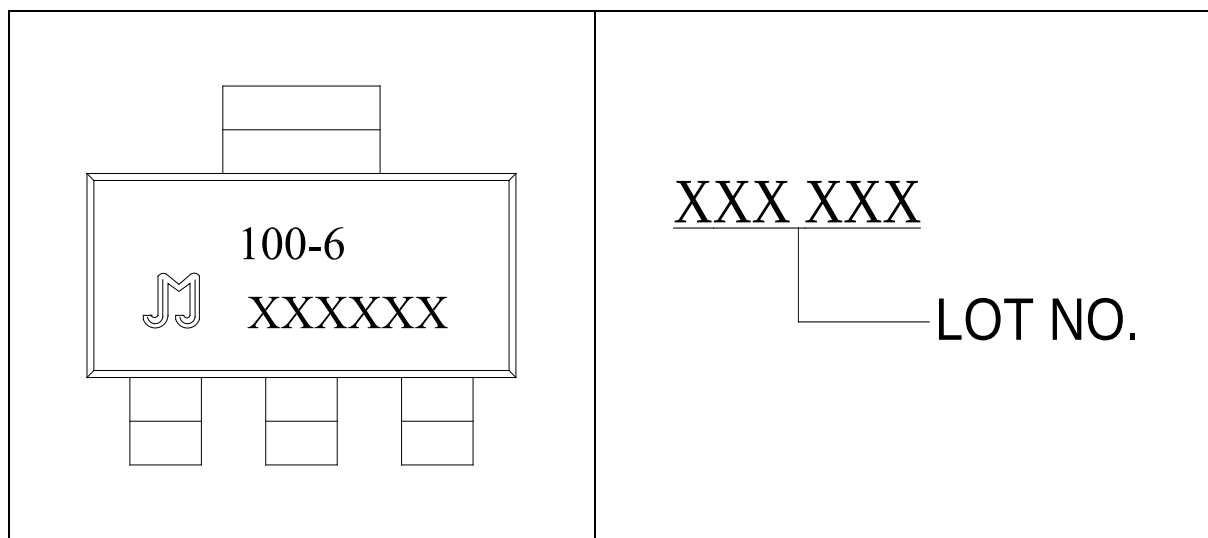


FIG.1: Maximum power dissipation versus RMS on-state current

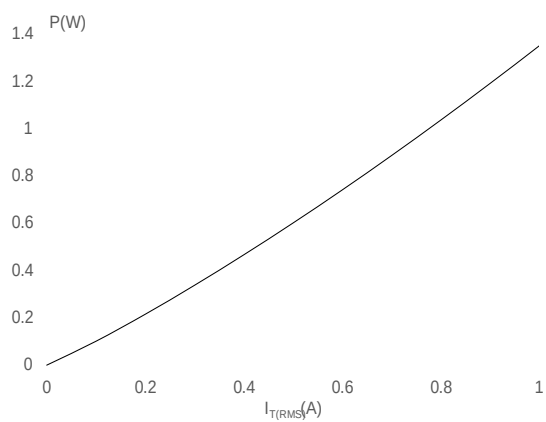


FIG.2: RMS on-state current versus case temperature

MCR100-6

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ORDERING INFORMATION

Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(μA)	Package	Base qty. (pcs)	Delivery mode
MCR100-6	800	≤ 200	SOT-223	4,000	Tape & Reel

Document Revision History

Date	Revision	Changes
May 19, 2023	A.1.0	Last update
Oct.23, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

PACKAGE MECHANICAL DATA

Dimensions

Milli(l)-7m [(s)] -11.2 ((s))TJ 02.4 6 0 Td [(hl-

DELIVERY MODE

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
W	-		12.30			
E	1.65	1.75	1.85			
F	5.45	5.50	5.55			
D0		1.55	1.60			
D1		-				
P0	3.90	4.00	4.10			
P1	7.90	8.00	8.10			
P2	1.95	2.00	2.05			
10P0		40 4.16±0.16	166 -2.51±0.25	2(4.10)±0.17	TJ -4.17 0[(36 -2.51	
A0						
B0						
K0						
T						

