

Peak pulse voltage (Tj=25 ; non-repetitive,off-state;FIG.7)	Vpp	2	kV
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ELECTRICAL CHARA W ; u Ñ ; C

ORDERING INFORMATION

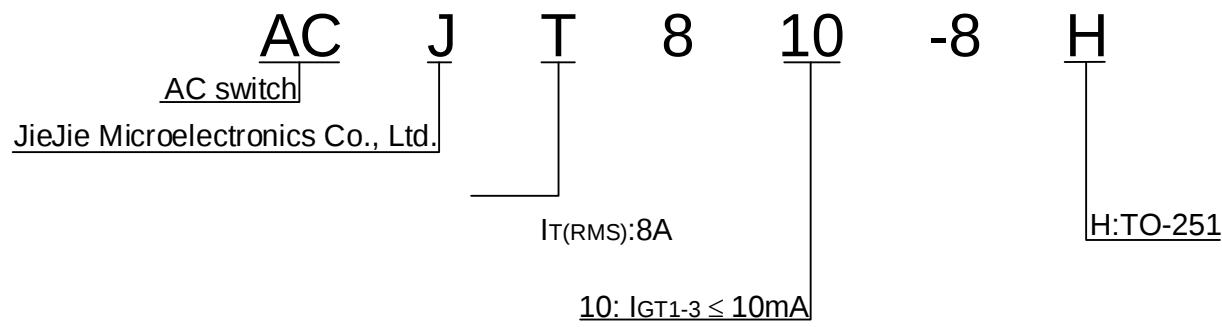


FIG.1: Maximum power dissipation versus RMS on-state current

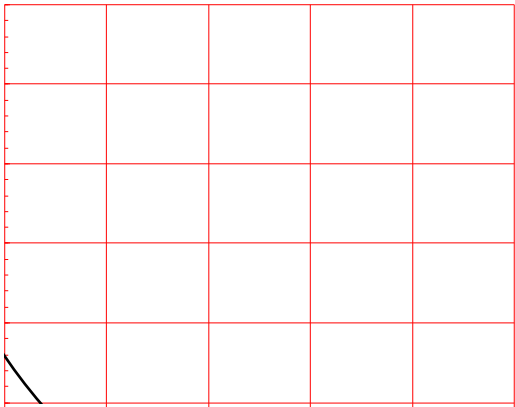
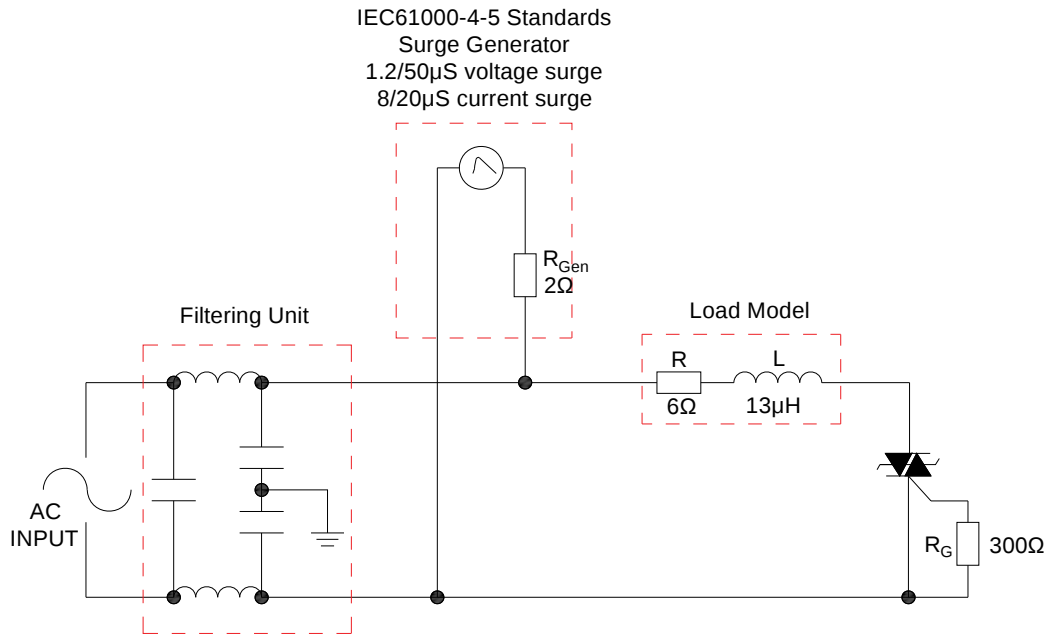


FIG.2: RMS on-state current versus case temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



LEAD FORMING AND SOLDERING

Refer to the application note “Assembly Instructions for Thyristors in Through-hole Package” released by JieJie

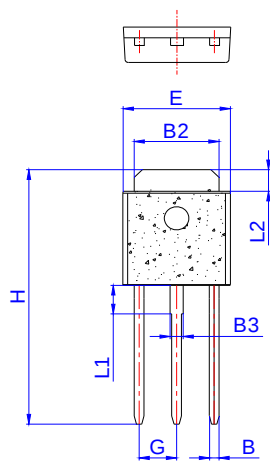
ORDERING INFORMATION

Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
ACJT810-8H	800	10	TO-251	80	Tube

Document Revision History

Date	Revision	Changes
Apr.13, 2023	A.1.0	Last updated
Oct.24, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.20					
A2	1.00					
B	0	s				
B2	3	0				Φ
B3						
C						
C2						
D						
E						
G						
L1						

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