



) -o#k@u@V

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Parameter		Symbol	Value	Unit

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of @#=@/8'ch-#@@° u@V'

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
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All Typical values at  $T_a=25$

$V_{in}$  : Input signal ( $f=25kHz, duty=50%, tr=tf=5ns$  or less).  $C_L$  is less than 15 pF which includes probe and stray wiring capacitance.

$V_{CMH}$  :  $CM_H$  is the maximum rate of fall of the common mode voltage that can be sustained with the output voltage in the logic high state ( $V_O = 2.6V$ ).

$V_{CML}$  :  $CML$  is the maximum rate of rise of the common mode voltage that can be sustained with the output voltage in the logic low state ( $V_O = 1V$ ).

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Characteristics	Symbol	Min.	Typ.	Max.	Unit

$V_{RO}$  : The recommended operating conditions are given as a design guide necessary to obtain the intended performance of the device. Each parameter is an independent value. When creating a system design using this device, the electrical characteristics specified in this datasheet should also be considered.

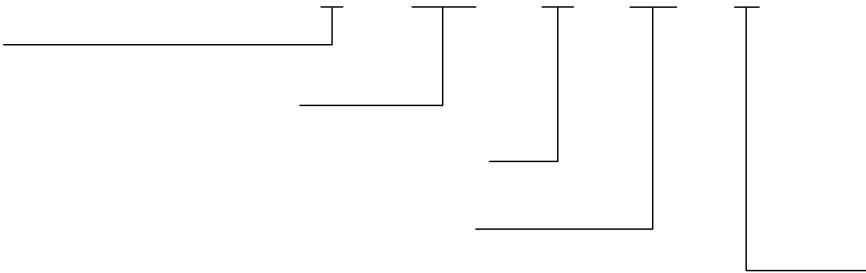
$V_{AC}$  : A ceramic capacitor (0.1μF) should be connected between pin 6 ( $V_{CC}$ ) and pin 4 (GND) to stabilize the operation of a high gain linear amplifier. Otherwise, this photocoupler may not switch properly. The bypass capacitor should be placed within 1 cm of each pin.

$V_{RI}$  : The rise and fall times of the input on current should be less than 0.5 μs.

$V_{RS}$  : If the rising slope of the supply voltage ( $V_{CC}$ ) for the detector is steep, stable operation of the internal circuits cannot be guaranteed. Be sure to set 3V/μs or less for a rising slope of the  $V_{CC}$ .

$V_{OR}$  : Denotes the operating range, not the recommended operating condition.

\k) -k@8@7\kU °u@V



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FIG.1:

FIG.2:

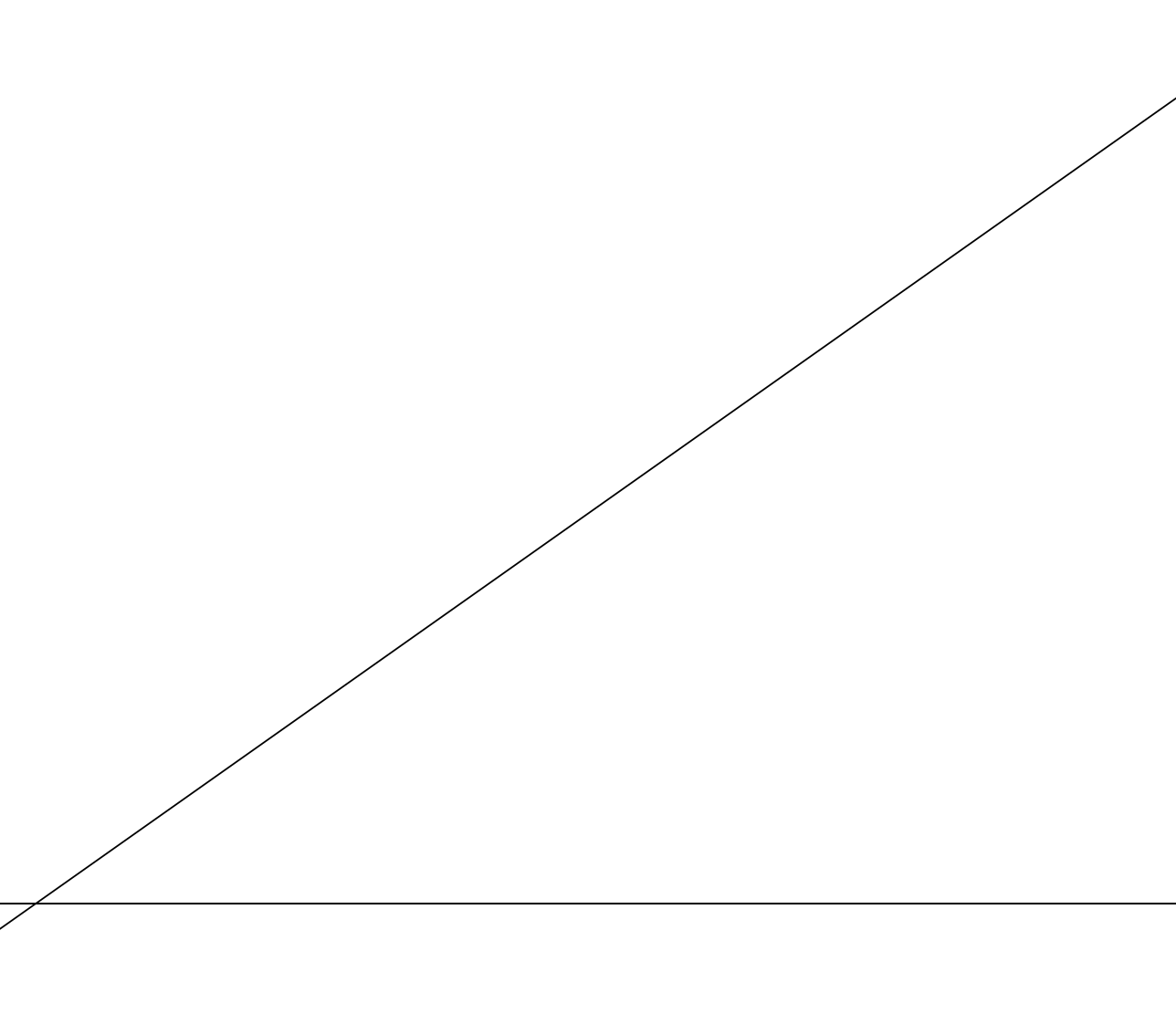
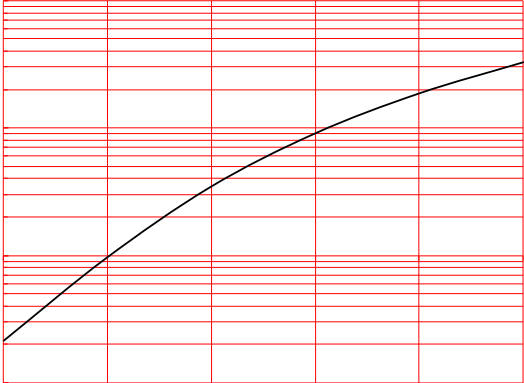
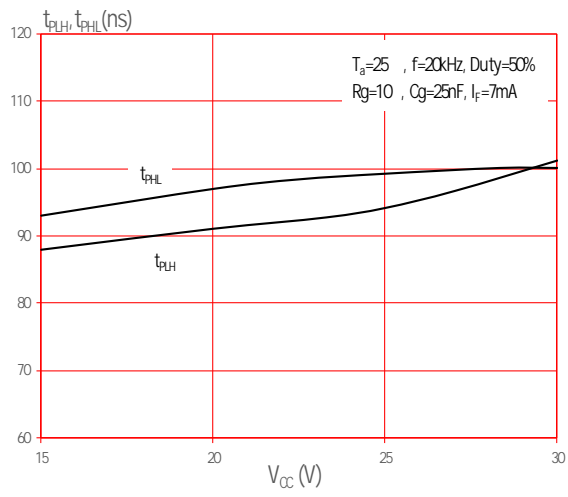


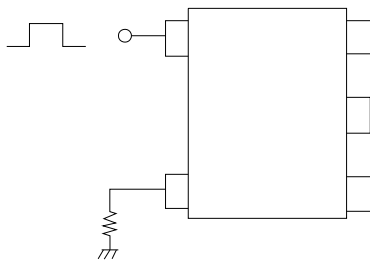


FIG.13:



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U #  
FIG.14:





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