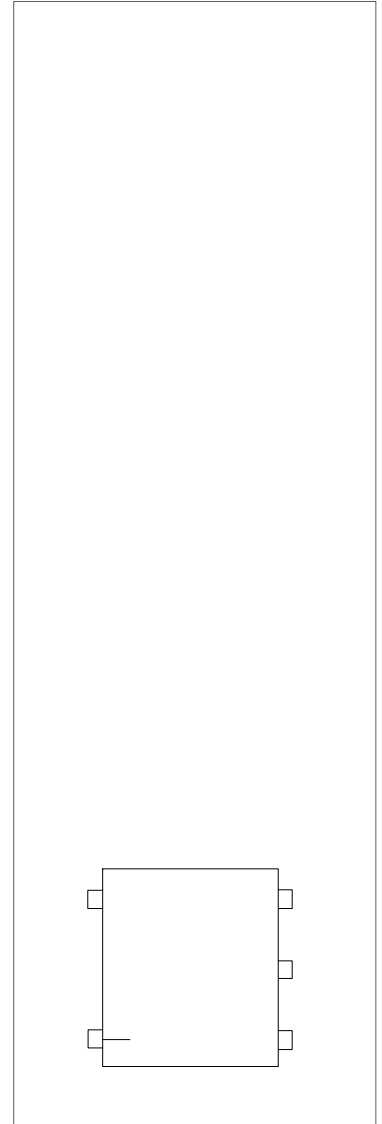




) -o#k@u@V

U ° @ 7- ° uyk-o



u u

° " o \ Q y u - U ° Q y U k ° u @ 8 o :

Parameter		Symbol	Value	Unit

V \ u : 100 s pulse, 100Hz frequency

V \ u : AC for 1minute, R.H.=40~60%

.

- Q # u k @ ° O # = ° k ° # u - k @ u # o

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit

--	--	--	--	--	--	--

All Typical values at $T_a=25$ -

V_{in} : Input signal ($f=25\text{kHz}$, $\text{duty}=50\%$, $t_r=t_f=5\text{ns}$ or less). C_L is less than 15 pF which includes probe and stray wiring capacitance.

V_{CMH} : CM_H is the maximum rate of fall of the common mode voltage that can be sustained with the output voltage in the logic high state ($V_O = 2.6\text{V}$).

V_{CML} : CML is the maximum rate of rise of the common mode voltage that can be sustained with the output voltage in the logic low state ($V_O = 1\text{V}$).

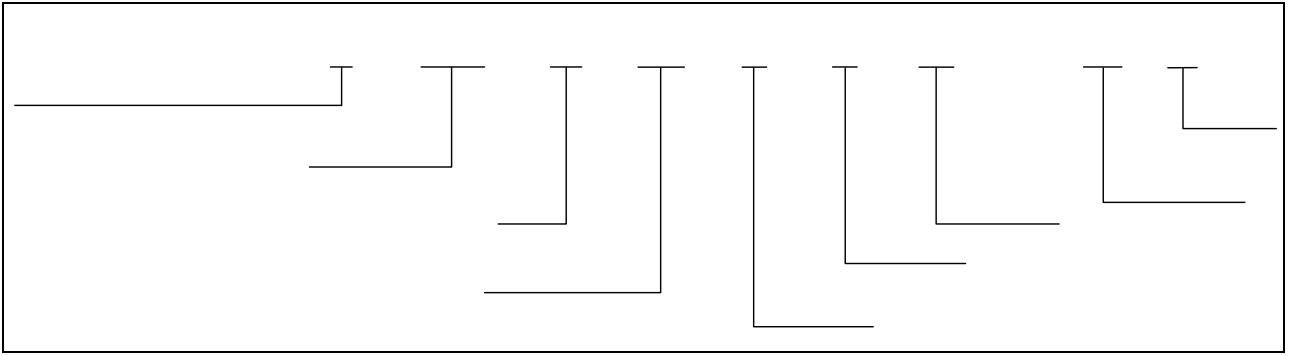
.

.

k \ #

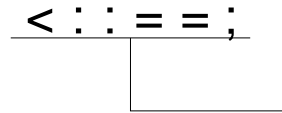
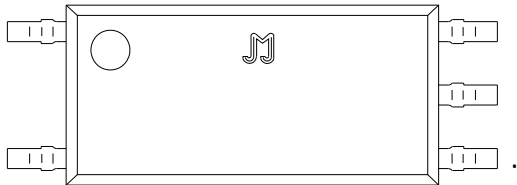
Characteristics	Symbol	Min.	Typ.	Max.	Unit

U[°] kM⁸



	h	j
\		j
None/R		3000 Units/Reel

U[°] kM⁸



#

#

FIG.7:

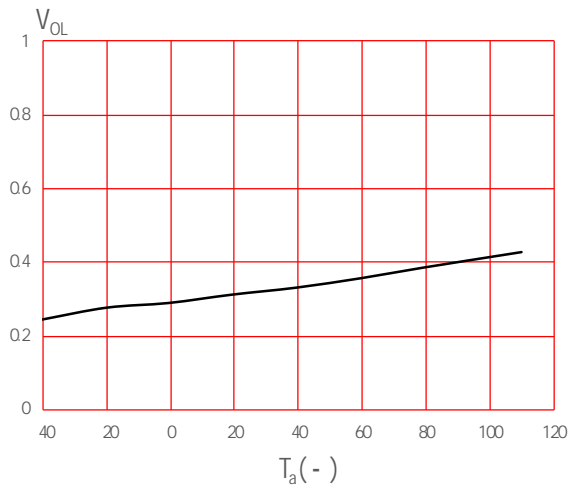
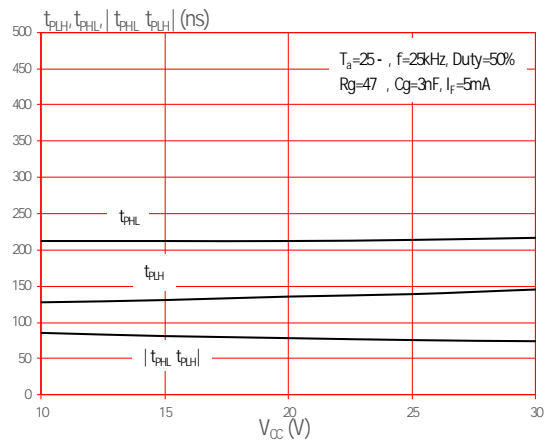
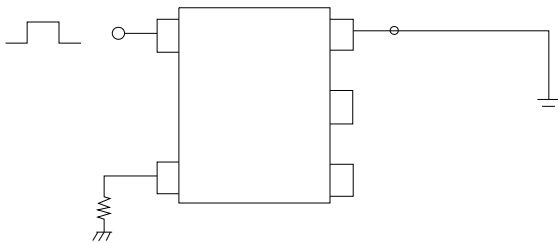


FIG.8:

FIG.13:

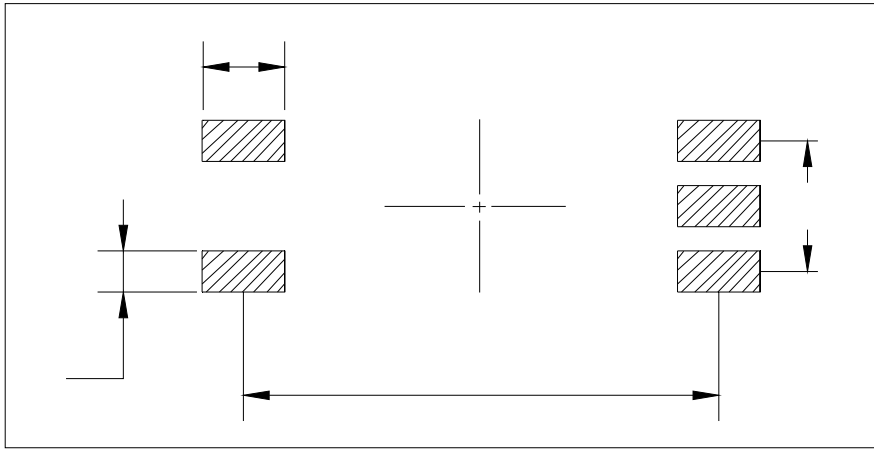


u #
FIG.14:

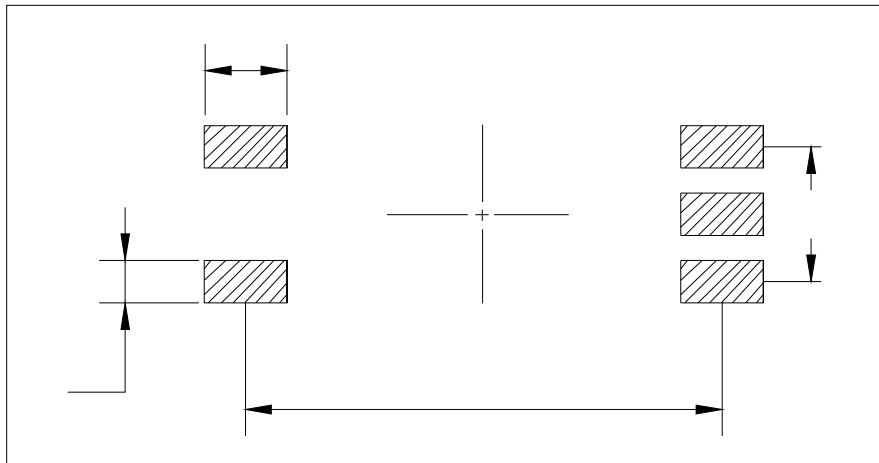


k-# \ U U - V) -) ' o \ Q - k U ° d M)

Q \ h



Q \ h †



k-70 ± @7 kU ° u@V

